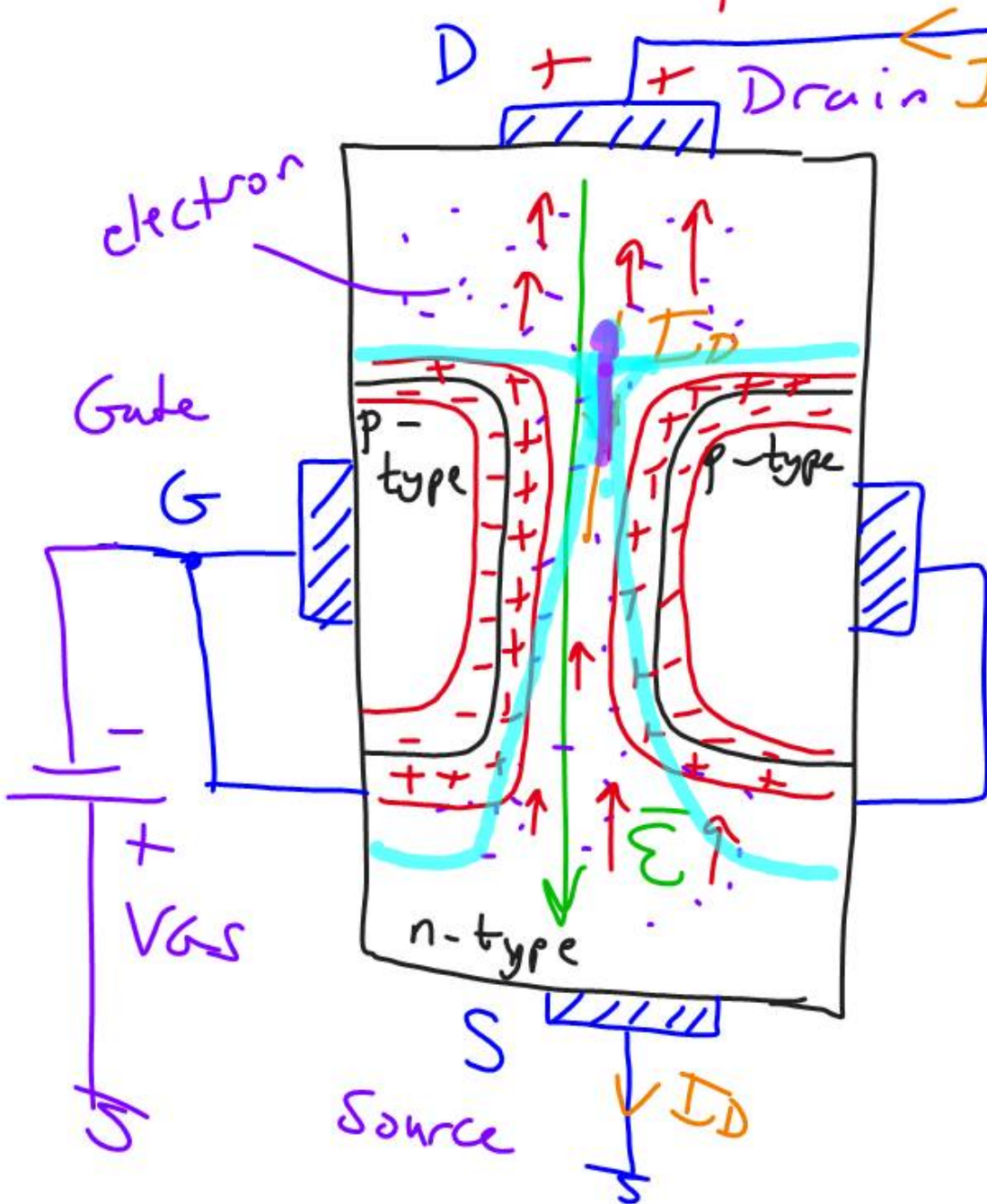


# JFET (Junction FET)

18.04.2011

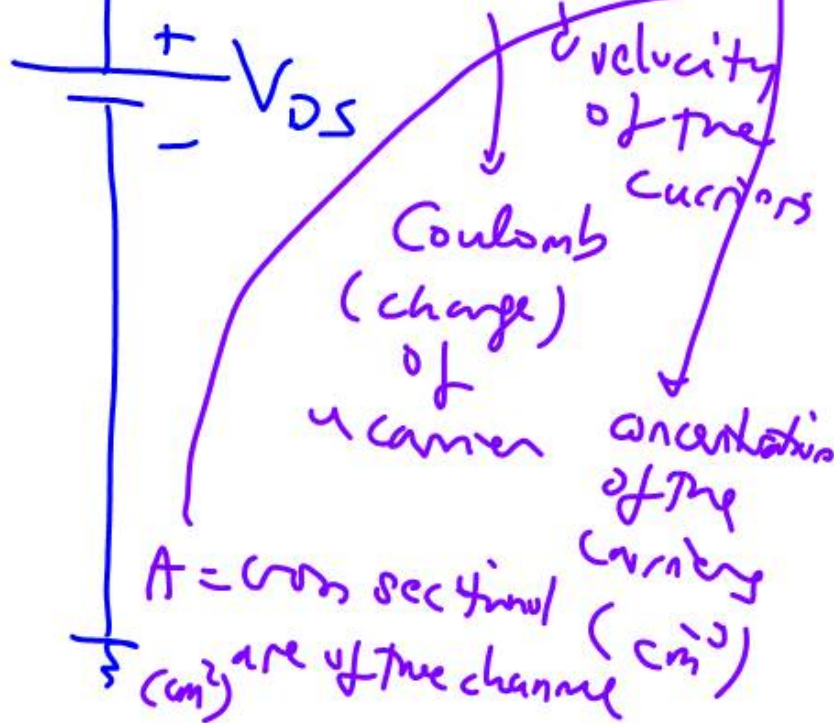
©

LF



a unipolar device

$$I = q \cdot v \cdot n \cdot A$$



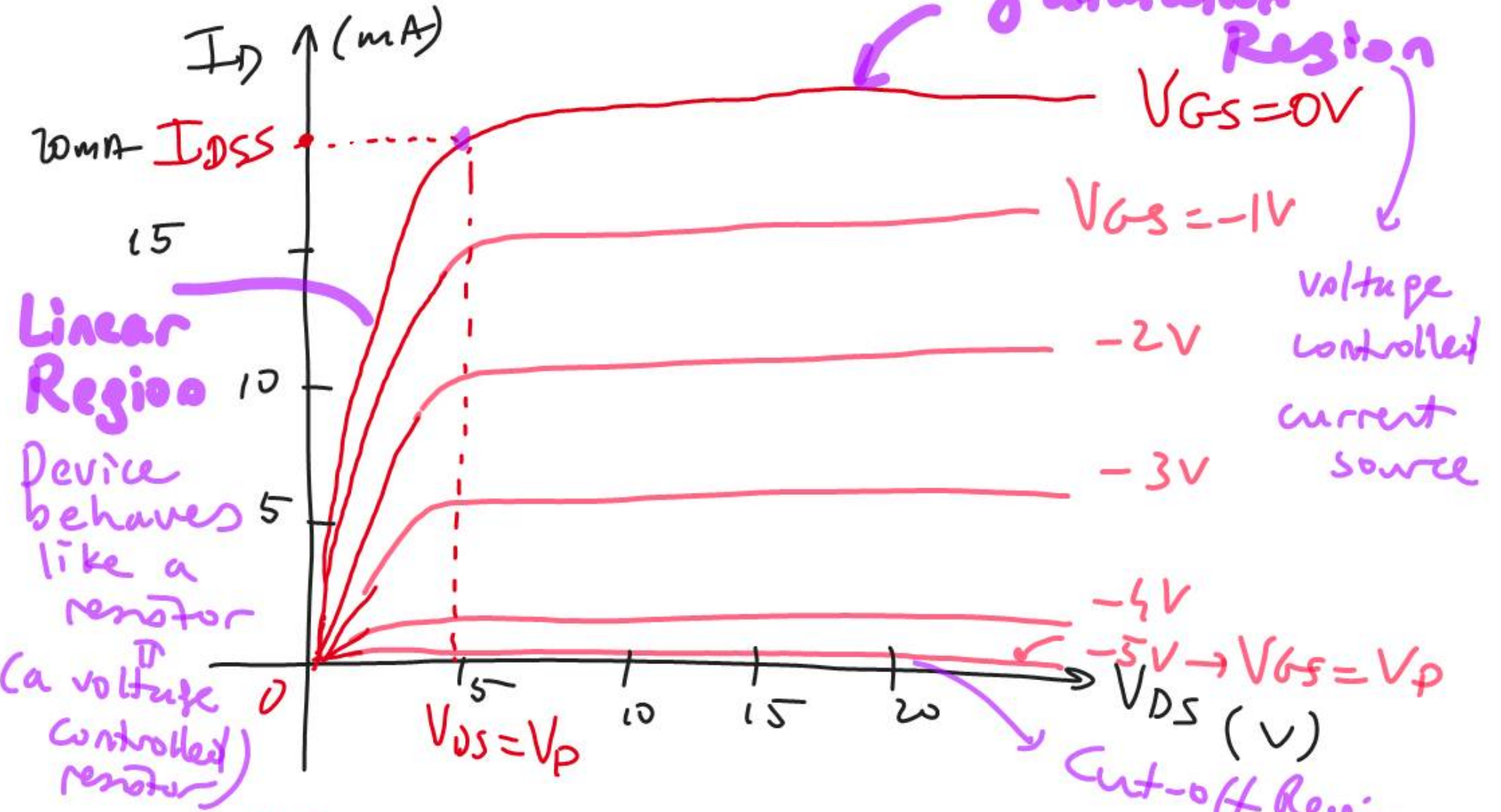
The channel's cross sectional area ( $A$ ) is controlled by the ( $V_{GS}$ ) reverse biased p n junctions.

as reverse bias voltage ( $V_{GS}$ ) increases  
The depth of the depletion region increases  
and the channel area ( $A$ ) decreases,  
as a result, since  $I = qn\mu A$ ,  $I$  decreases.

$I_D$  = Drain current

n-channel JFET  
The output characteristics

Device behaves as a current source  
**Saturation Region**



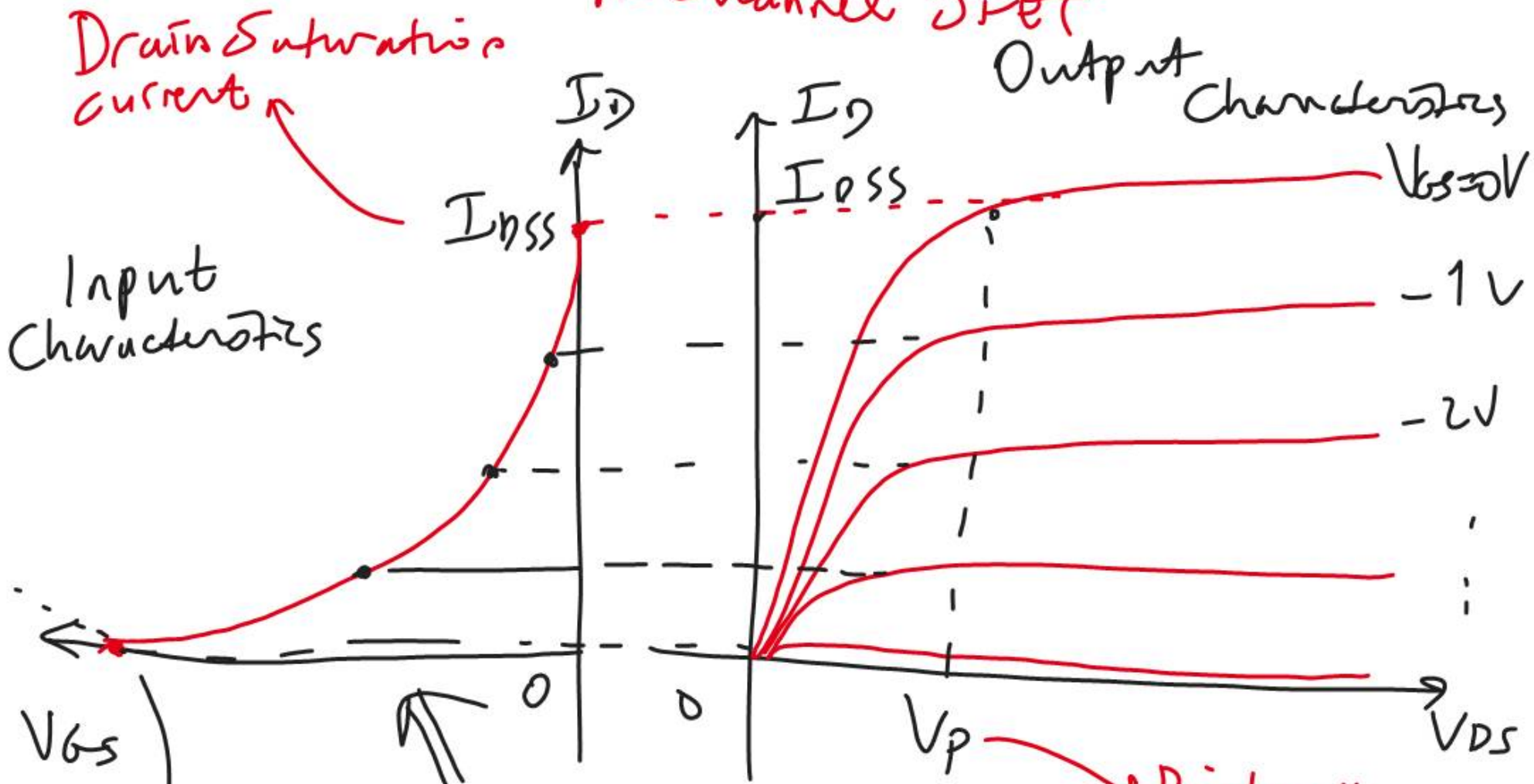
**Linear Region**  
Device behaves like a resistor  
(a voltage controlled resistor)

$V_{GS} = 0V$   
 $V_{GS} = -1V$   
 $-2V$   
 $-3V$   
voltage controlled current source

$V_p =$  pinch-off voltage

$-4V$   
 $-5V \rightarrow V_{GS} = V_p$   
 $V_{DS}$  (V)  
Cut-off Region

# n-channel JFET



$V_{GS}$   
 $V_{GS} = -|V_p|$

$$I_D = I_{DSS} \left( 1 - \frac{V_{GS}}{V_p} \right)^2$$

Shockley's equation

\* when  $V_{GS} = 0V$ ,  
 $I_D = I_{DSS}$   
 \* when  $V_{GS} = -|V_p|$   
 $I_D = 0$